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TORNEY DOCKET NO	CONFIDMATION

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/762,280	01/23/2004	Manfred Proell	INF-133A	4704
48154	7590 11/17/2005		EXAMINER	
SLATER &	MATSIL LLP		AUDUONG, G	ENE NGHIA
17950 PREST	TON ROAD			
SUITE 1000			ART UNIT	PAPER NUMBER
DALLAS, T	X 75252		2827	

DATE MAILED: 11/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		App	olication No.	Applicant(s)			
		10/	762,280	PROELL ET AL.			
	Office Action Summary	Exa	miner	Art Unit			
			ne N. Auduong	2827			
Period fo	The MAILING DATE of this commun or Reply	nication appears	on the cover sheet v	with the correspondence ac	ddress		
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE IN INSIGN SOLUTION OF THE INSIGN (6) MONTHS from the mailing date of this common of the property of the maximum is the toreply within the set or extended period for reply received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	MAILING DATE (s of 37 CFR 1.136(a). munication. tatutory period will apply y will, by statute, cause	OF THIS COMMUN In no event, however, may a ly and will expire SIX (6) MO the application to become A	IICATION. The reply be timely filed ONTHS from the mailing date of this of the case of this case. ABANDONED (35 U.S.C. § 133).			
Status							
1)[Responsive to communication(s) file	ed on .					
•	•	2b)⊠ This actio	on is non-final.				
3)□	Since this application is in condition	for allowance e	xcept for formal ma	itters, prosecution as to the	e merits is		
,	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4) 又	Claim(s) <u>1-10</u> is/are pending in the	application.					
-	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)□	Claim(s) is/are allowed.						
6)⊠	Claim(s) <u>1-10</u> is/are rejected.						
7)	Claim(s) is/are objected to.						
8)□	Claim(s) are subject to restri	ction and/or elec	ction requirement.				
Applicat	ion Papers						
9)□	The specification is objected to by the	ne Examiner.					
10)[The drawing(s) filed on is/are	: a) accepted	l or b)⊡ objected to	b by the Examiner.			
	Applicant may not request that any object	ection to the drawi	ng(s) be held in abeya	ance. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority (ınder 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a)	a)⊠ All b)□ Some * c)□ None of: 1.⊠ Certified copies of the priority documents have been received.						
	Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No						
	3. Copies of the certified copies of the priority documents have been received in this National Stage						
	application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.							
Attachmer	ıt(s)						
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)							
2) Notice	2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date						
	3) Anformation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 8-20-2004. 5) Notice of Informal Patent Application (PTO-152) 6) Other:						

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DETAILED ACTION

Priority

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

The information disclosure statement (IDS) submitted on August 20, 2004 is being considered by the examiner.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-10 are rejected under 35 U.S.C. 102(a) as being anticipated by Hsu et al. (U.S. Pat. No. 6,504,777).

Regarding claim 1, Hsu et al. disclose a RAM store having a shared SA structure (figure 2, sense amplifier shared by upper and lower memory array), comprising: sense amplifiers (SA) arranged in SA strips between two respective adjacent cell blocks for use by a plurality of bit line pairs from the adjacent cell blocks (figure 2, sense amplifier shared by upper and lower memory array), wherein the bit line pairs have respective charge equalization circuits individually (figure 3, equalizer 50) associated with them for the purpose of performing charge equalization between bit line halves of the bit line pairs in a precharge phase; and a shorting transistor (figure 3, transistor 110) that, when prompted by a control signal, connects the bit line halves of the bit line

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pairs that are in the precharge phase to one another, wherein the shorting transistor is arranged in or on the respective sense amplifier jointly for all bit line pairs (figures 1-3).

Regarding claim 2, Hsu et al. disclose the RAM store as in claim 1, wherein the shorting transistor 110 can be connected to a respective sense amplifier, and it can be switched by a separate shorting control signal (DQRST) via a dedicated control line (figure 3).

Regarding claims 3-4, Hsu et al. disclose the RAM store as in claim 1, wherein a respective sense amplifier can be connected to a respective one of two bit line pairs from a left-hand and a right-hand adjacent cell block (col. 4, lines 10+).

Regarding claim 5, Hsu et al. disclose the RAM store as in claim 1, wherein the separate shorting control signal supplied via the control line dedicated to the shorting transistors switches all of the shorting transistors in an SA strip (col. 4, lines 42+; col. 3, lines 30+).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gene N. Auduong whose telephone number is (571) 272-1773. The examiner can normally be reached on 9-5-4, alternate second Monday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GA November 10, 2005

> Gene N Auduong Primary Examiner Art Unit 2827